

BV Mapping

| Azimuth angle | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
|---------------------------|---------------------------|--------|--------|--------|------------|--------|--------|--------|---------------------------|--------|--------|--------|------------|--------|--------|--------|---------------------------|--------|--------|--------|------------|--------|--------|--------|---------------------------|--------|--------|--------|------------|--------|--------|--------|---------------------------|--------|--------|--------|------------|--------|--------|--------|---------------------------|--------|--------|--------|------------|--------|--------|--------|---------------------------|--------|--------|--------|------------|--------|--------|--------|---------------------------|--------|--------|--------|------------|--------|--------|--------|----|----|----|----|----|----|---|---|----|----|----|---|---|---|---|---|
| Corresponding AWB # | AWB #07 | | | | AWB #06 | | | | AWB #05 | | | | AWB #04 | | | | AWB #03 | | | | AWB #02 | | | | AWB #01 | | | | AWB #00 | | | | AWB #15 | | | | AWB #14 | | | | AWB #13 | | | | AWB #12 | | | | AWB #11 | | | | AWB #10 | | | | AWB #9 | | | | AWB #8 | | | | | | | | | | | | | | | | | | | |
| TDC Channel | 9 | 11 | 13 | 15 | 1 | 3 | 5 | 7 | 9 | 11 | 13 | 15 | 1 | 3 | 5 | 7 | 9 | 11 | 13 | 15 | 1 | 3 | 5 | 7 | 9 | 11 | 13 | 15 | 1 | 3 | 5 | 7 | 9 | 11 | 13 | 15 | 1 | 3 | 5 | 7 | 9 | 11 | 13 | 15 | 1 | 3 | 5 | 7 | 9 | 11 | 13 | 15 | 1 | 3 | 5 | 7 | 9 | 11 | 13 | 15 | 1 | 3 | 5 | 7 | 9 | 11 | 13 | 15 | 1 | 3 | 5 | 7 | | | | | | | | |
| TDC: FPGA and connector # | TDC FPGA 04_Connecteur 03 | | | | | | | | TDC FPGA 04_Connecteur 02 | | | | | | | | TDC FPGA 04_Connecteur 01 | | | | | | | | TDC FPGA 03_Connecteur 03 | | | | | | | | TDC FPGA 03_Connecteur 02 | | | | | | | | TDC FPGA 03_Connecteur 01 | | | | | | | | TDC FPGA 02_Connecteur 02 | | | | | | | | TDC FPGA 02_Connecteur 01 | | | | | | | | | | | | | | | | | | | | | | | |
| ADC Channel | 4 | 5 | 6 | 7 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 0 | 1 | 2 | 3 | | | | | | | | |
| ADC # | ADC 16 | | | | | | | | ADC 17 | | | | | | | | ADC 14/ RTM 05 | | | | | | | | ADC 13 / RTM 04 | | | | | | | | ADC 12 / RTM 03 | | | | | | | | ADC 11 / RTM 02 | | | | | | | | ADC 10 / RTM 01 | | | | | | | | ADC 09 / RTM 00 | | | | | | | | | | | | | | | | | | | | | | | |
| ASD Position_# | ASD TOP_15 | | | | ASD TOP_14 | | | | ASD TOP_13 | | | | ASD TOP_12 | | | | ASD TOP_11 | | | | ASD TOP_10 | | | | ASD TOP_09 | | | | ASD TOP_08 | | | | ASD TOP_07 | | | | ASD TOP_06 | | | | ASD TOP_05 | | | | ASD TOP_04 | | | | ASD TOP_03 | | | | ASD TOP_02 | | | | ASD TOP_01 | | | | ASD TOP_00 | | | | | | | | | | | | | | | | | | | |
| ASD connector # | j1 | j2 | j3 | j4 | j1 | j2 | j3 | j4 | j1 | j2 | j3 | j4 | j1 | j2 | j3 | j4 | j1 | j2 | j3 | j4 | j1 | j2 | j3 | j4 | j1 | j2 | j3 | j4 | j1 | j2 | j3 | j4 | j1 | j2 | j3 | j4 | j1 | j2 | j3 | j4 | j1 | j2 | j3 | j4 | j1 | j2 | j3 | j4 | j1 | j2 | j3 | j4 | j1 | j2 | j3 | j4 | j1 | j2 | j3 | j4 | j1 | j2 | j3 | j4 | | | | | | | | | | | | | | | | |
| BAR # | BAR 63 | BAR 62 | BAR 61 | BAR 60 | BAR 59 | BAR 58 | BAR 57 | BAR 56 | BAR 55 | BAR 54 | BAR 53 | BAR 52 | BAR 51 | BAR 50 | BAR 49 | BAR 48 | BAR 47 | BAR 46 | BAR 45 | BAR 44 | BAR 43 | BAR 42 | BAR 41 | BAR 40 | BAR 39 | BAR 38 | BAR 37 | BAR 36 | BAR 35 | BAR 34 | BAR 33 | BAR 32 | BAR 31 | BAR 30 | BAR 29 | BAR 28 | BAR 27 | BAR 26 | BAR 25 | BAR 24 | BAR 23 | BAR 22 | BAR 21 | BAR 20 | BAR 19 | BAR 18 | BAR 17 | BAR 16 | BAR 15 | BAR 14 | BAR 13 | BAR 12 | BAR 11 | BAR 10 | BAR 09 | BAR 08 | BAR 07 | BAR 06 | BAR 05 | BAR 04 | BAR 03 | BAR 02 | BAR 01 | BAR 00 | | | | | | | | | | | | | | | | |
| ASD connector # | j4 | j3 | j2 | j1 | j4 | j3 | j2 | j1 | j4 | j3 | j2 | j1 | j4 | j3 | j2 | j1 | j4 | j3 | j2 | j1 | j4 | j3 | j2 | j1 | j4 | j3 | j2 | j1 | j4 | j3 | j2 | j1 | j4 | j3 | j2 | j1 | j4 | j3 | j2 | j1 | j4 | j3 | j2 | j1 | j4 | j3 | j2 | j1 | j4 | j3 | j2 | j1 | j4 | j3 | j2 | j1 | j4 | j3 | j2 | j1 | j4 | j3 | j2 | j1 | j4 | j3 | j2 | j1 | | | | | | | | | | | | |
| ASD Position_# | ASD BOT_15 | | | | ASD BOT_14 | | | | ASD BOT_13 | | | | ASD BOT_12 | | | | ASD BOT_11 | | | | ASD BOT_10 | | | | ASD BOT_09 | | | | ASD BOT_08 | | | | ASD BOT_07 | | | | ASD BOT_06 | | | | ASD BOT_05 | | | | ASD BOT_04 | | | | ASD BOT_03 | | | | ASD BOT_02 | | | | ASD BOT_01 | | | | ASD BOT_00 | | | | | | | | | | | | | | | | | | | |
| ADC # | ADC 16 | | | | | | | | ADC 17 | | | | | | | | ADC 14/ RTM 05 | | | | | | | | ADC 13 | | | | | | | | ADC 12 | | | | | | | | ADC 11 | | | | | | | | ADC 10 | | | | | | | | ADC 09 | | | | | | | | | | | | | | | | | | | | | | | |
| ADC Channel | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | | | | | | | | |
| TDC: FPGA and connector # | TDC FPGA 04_Connecteur 03 | | | | | | | | TDC FPGA 04_Connecteur 02 | | | | | | | | TDC FPGA 04_Connecteur 01 | | | | | | | | TDC FPGA 03_Connecteur 03 | | | | | | | | TDC FPGA 03_Connecteur 02 | | | | | | | | TDC FPGA 03_Connecteur 01 | | | | | | | | TDC FPGA 02_Connecteur 02 | | | | | | | | TDC FPGA 02_Connecteur 01 | | | | | | | | | | | | | | | | | | | | | | | |
| TDC Channel | 14 | 12 | 10 | 8 | 6 | 4 | 2 | 0 | 14 | 12 | 10 | 8 | 6 | 4 | 2 | 0 | 14 | 12 | 10 | 8 | 6 | 4 | 2 | 0 | 14 | 12 | 10 | 8 | 6 | 4 | 2 | 0 | 14 | 12 | 10 | 8 | 6 | 4 | 2 | 0 | 14 | 12 | 10 | 8 | 6 | 4 | 2 | 0 | 14 | 12 | 10 | 8 | 6 | 4 | 2 | 0 | 14 | 12 | 10 | 8 | 6 | 4 | 2 | 0 | 14 | 12 | 10 | 8 | 6 | 4 | 2 | 0 | 14 | 12 | 10 | 8 | 6 | 4 | 2 | 0 |